

To the Specification

Please amend paragraph [0037] of the specification as follows:

[0037] FIG. 1 is a block diagram showing a preferred embodiment of the multi-memory architecture according to the invention, which is composed of two memory devices including a 64Mb Mask ROM 100, an 8Mb flash memory 102, and a controller 104. In the invention, the overall pin configuration of this multi-memory architecture is arranged to be the same as the pin configuration of the generic 64Mb ~~flash memory~~ Mask ROM 100 for ~~compatibility~~ compatibility.